This FDP is arranged by N.K. Orchid College of Engineering and Technology in association with in association with Trident Techlabs Pvt. Ltd. , Pune.

The objective of this FDP is to provide an opportunity for participants to enrich their knowledge and skills in developing various solutions for solving Engineering problems in the society. This program serves as a platform for faculty, research scholars, engineers and students to interact on cutting edge technology in VLSI. The FDP covers Introduction to Mentor Graphics , Design flow ,Hands on session of TANNER and Calibre. The FDP also covered FPGA synthesis, Leonardo spectrum tool, various applications of VLSI and ASIC design flow.The partcipants present are faculty from various institute and Solapur university.

The coordinator of the program are Dr. P. D. Bahirgonde and Prof. V. S. Shirwal.