

### Hands-on Workshop on “VLSI Design using Verilog HDL” on 21 August 2019.

The webinar is arranged for B.E.(E & TC) students with the objective of introducing Industry graded EDA Tools and focus on overall development of students by training them on the advanced skills i.e. System Verilog, UVM etc. with Industry standard projects and prepare them considering the requirements of design houses. The session on VLSI design and Verilog Coding is delivered by Industry Expert Mr. P R Sivakumar,CEO - Maven Silicon.This workshop covers Overview of VLSI Design,IPs, Chips and SoCs,SoC Design,ASIC Vs FPGA, RTL Design using Verilog HDL, data types, operators etc.